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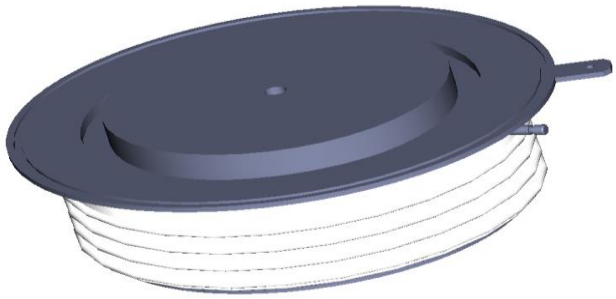
http://gpsemi.it/pdf/GPTR1494.pdf



GPTR1494

PHASE CONTROLLED SCR

High reliability operation
Electroplating applications
Resistance welding applications

BLOCKING VOLTAGE UP TO	**800**	**V**
AVERAGE CURRENT	**4940**	**A**
SURGE CURRENT	**50**	**kA**

BLOCKING CHARACTERISTICS

Characteristic		Conditions	Value	
V_{RRM}	Repetitive peak reverse voltage		800	V
V_{RSM}	Non-repetitive peak reverse voltage		900	V
V_{DRM}	Repetitive peak off-state voltage		800	V
I_{DRM}	Repetitive peak off-state current, max.	V_{DRM}, single phase, half wave, Tj = Tjmax	200	mA
I_{RRM}	Repetitive peak reverse current, max.	V_{RRM}, single phase, half wave, Tj = Tjmax	200	mA

ON-STATE CHARACTERISTICS

$I_{T(AV)}$	Average on-state current	Sine wave,180° conduction, Th = 55 °C	4940	A
$I_{T(RMS)}$	R.M.S. on-state current	Sine wave,180° conduction, Th = 55 °C	7760	A
I_{TSM}	Surge on-state current	Non rep. half sine wave, 50 Hz, V_R = 0 V, $T_j = T_{jmax}$	50	kA
I^2t	I^2 t for fusing coordination		12500	kA^2s
$V_{T(TO)}$	Threshold voltage	$T_j = T_{jmax}$	0.87	V
r_T	On-state slope resistance	$T_j = T_{jmax}$	0.06	mΩ
V_{TM}	Peak on-state voltage, max	On-state current I_T = 5000 A , Tj = Tjmax	1.17	V
I_H	Holding current, max	T_j = 25 °C	170	mA
I_L	Latching current, typ	T_j = 25 °C	1500	mA

TRIGGERING CHARACTERISTICS

V_{GT}	Gate trigger voltage	T_j = 25 °C, V_D = 12 V	3	V
I_{GT}	Gate trigger current	T_j = 25 °C, V_D = 12 V	250	mA
V_{GD}	Non-trigger voltage	V_D = 67% V_{RRM}, $T_j = T_{jmax}$	0.2	V
P_{GM}	Peak gate power dissipation	Pulse width 0.5 ms	100	W
$P_{G(AV)}$	Average gate power dissipation		5	W
I_{FGM}	Peak gate current		10	A
V_{FGM}	Peak gate voltage (forward)		12	V
V_{RGM}	Peak gate voltage (reverse)		10	V

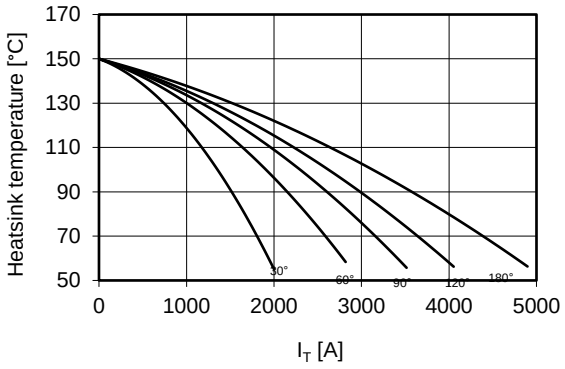
SWITCHING CHARACTERISTICS

di/dt	Critical rate of rise of on-state current	$T_j = T_{jmax}$	200	A/µs
dV/dt	Critical rate of rise of off-state voltage	$T_j = T_{jmax}$	1000	V/µs
t_q	Turn-off time, typ	$T_j = T_{jmax}$, I_T = 4000 A, di/dt = -12.5 A/µs VR = 100 V, VD = 67% VDRM, dV/dt = 20 V/µs	300	µs

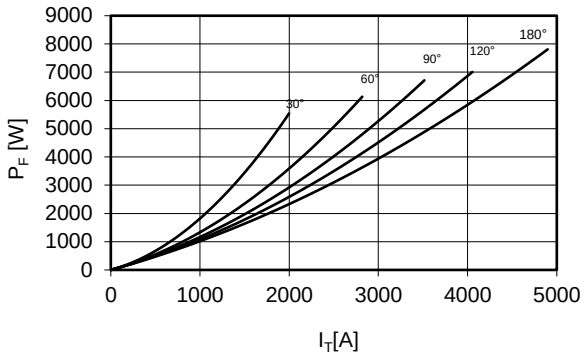
THERMAL AND MECHANICAL CHARACTERISTICS

$R_{th(j-c)}$	Thermal resistance (junction to case)	Double side cooled	0.010	°C/W
$R_{th(c-h)}$	Thermal resistance (case to heatsink)	Double side cooled	0.002	°C/W
T_{jmax}	Max operating junction temperature		150	°C
T_{stg}	Storage temperature		-40 / 150	°C
F	Clamping force ± 10%		40	kN
	Mass		950	g

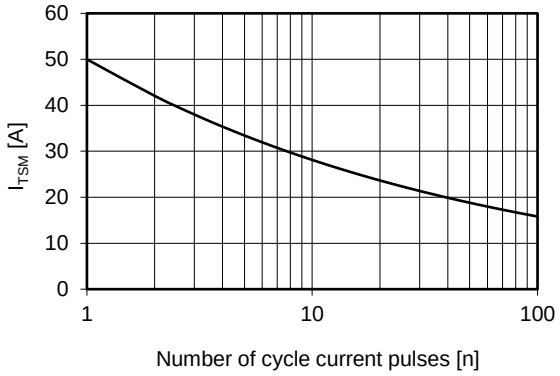
Current rating - sine wave



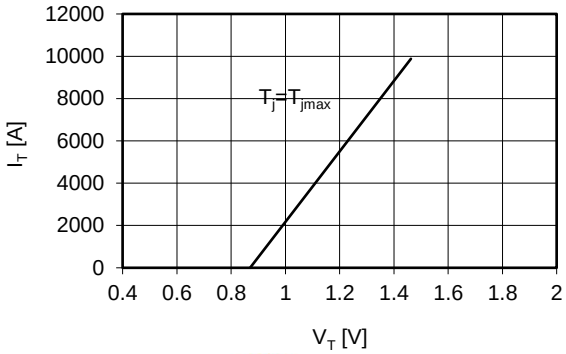
Power loss - sine wave



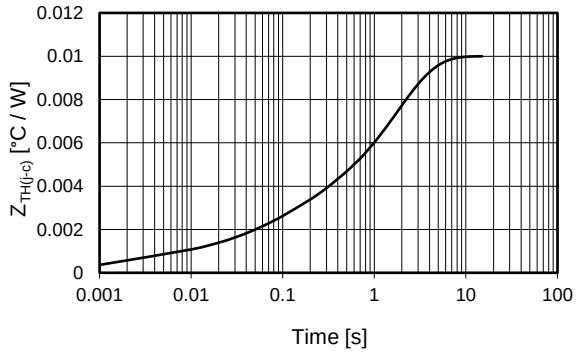
Maximum surge current d.s. cooled

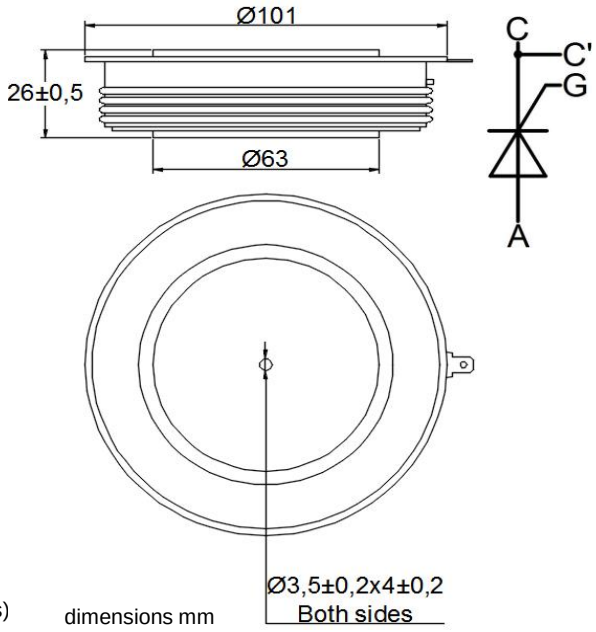


On-state voltage drop



Thermal impedance (j-c)





Ordering information GPTR1494-VVGL

VV: blocking voltage / 100 (e.g. 08 for 800 V)

G: trigger lead type (**S** = straight **T** = twisted **blank** = no leads)

L: trigger lead lenght x 100mm (**3 - 4 - 5 - 7 blank** = no leads)